

AMENDMENTS TO THE SPECIFICATION:

Page 8, replace the paragraph beginning on line 16 with the following rewritten paragraph:

--According to one aspect of the present invention, a liquid crystal display comprises a first substrate, a second substrate opposing to the first substrate, and liquid crystal provided in a space between the first substrate and second substrate. The first substrate includes a transparent substrate, a plurality of gate lines and a plurality of drain lines formed on the transparent substrate, thin film transistors, pixel electrodes, ~~and electrodes~~ electrodes connected to the thin film transistor, and common electrodes. Each of the thin film transistors is provided in a vicinity of the intersection of the gate line and drain line. Each of the pixel electrodes is formed within a pixel region enclosed with an adjacent pair of the gate lines and an adjacent pair of the drain lines. Each of the common electrodes develops an electric field within each of the pixel regions between the pixel electrode and itself. The second substrate includes color layers provided for each of the pixel regions. The color layers are spaced apart from the gate lines and drain lines when seen on a plane.--

Page 11, replace the paragraph beginning on line 16 with the following amended paragraph:

--Fig. 8 is a ~~plane~~ plan view showing a constitution of a liquid crystal display in accordance with a first embodiment of the present invention;--

Page 16, replace the paragraph beginning on line 3 with the following amended paragraph:

--In the present embodiment, as shown in Fig. 8, the color layer 12 is provided separately for each pixel. Further, each color layer 12 is positioned so as not to overlap the gate line 6 nor drain line 5 when seen from the normal direction of the substrate in ~~plane~~ plan view. In other words, as shown in Fig. 9, the black matrix layer 13 is formed to extend to reach above the common electrode 2 on the drain line 5 side, and the color layer 12 is formed to extend but not to reach the drain line 5. The color layer 12 is preferably formed so as to overlap the black matrix layer 13 at a position spaced apart by a distance of 6 μm from the drain line 5 in a horizontal direction with respect to the surface of the substrate. Also, as shown in Fig. 10, the black matrix layer 13 is formed so as to cover the gate line 6 and extend to reach a coupled portion of the common electrode 2 on the gate line 6 side, while the color layer 12 is formed to extend but not to reach the gate line 6. The color layer 12 is preferably formed so as to overlap the black matrix layer 13 at a position

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spaced apart by a distance of 6 μm from the gate line 6 in a horizontal direction with respect to the surface of the substrate.--